

3-31-03

EAST - [Dwin_Default.wsp:1]

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Drafts Pending Active

- L1: (78) string adj model
- L2: (10) 1 and (circuit same simulat\$4)
- L3: (143) (lumped same elements) and (circuit same simulat\$4) and
(ramp same time)
- L4: (0) 3 and (string same model)
- L5: (17) 3 and string and model
- L6: (48) 3 and (noise same voltage)
- L7: (47) 6 and capaci\$5
- L8: (41) 6 and capacitor
- L9: (7) (circuit same simulat\$4) and (pi same model) and (ramp

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	U	I	Document ID	Issue Date	Pages	Title
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6536022 B1	20030318	15	Two pole coupling noise analysis in submicron integrated circuit
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6507935 B1	20030114	14	Method of analyzing crosstalk in integrated circuit
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6449753 B1	20020910	15	Hierarchical coupling noise analysis in submicron integrated circuit
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6353917 B1	20020305	14	Determining a worst case scenario in integrated circuit design

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